

FIG. 1
related art

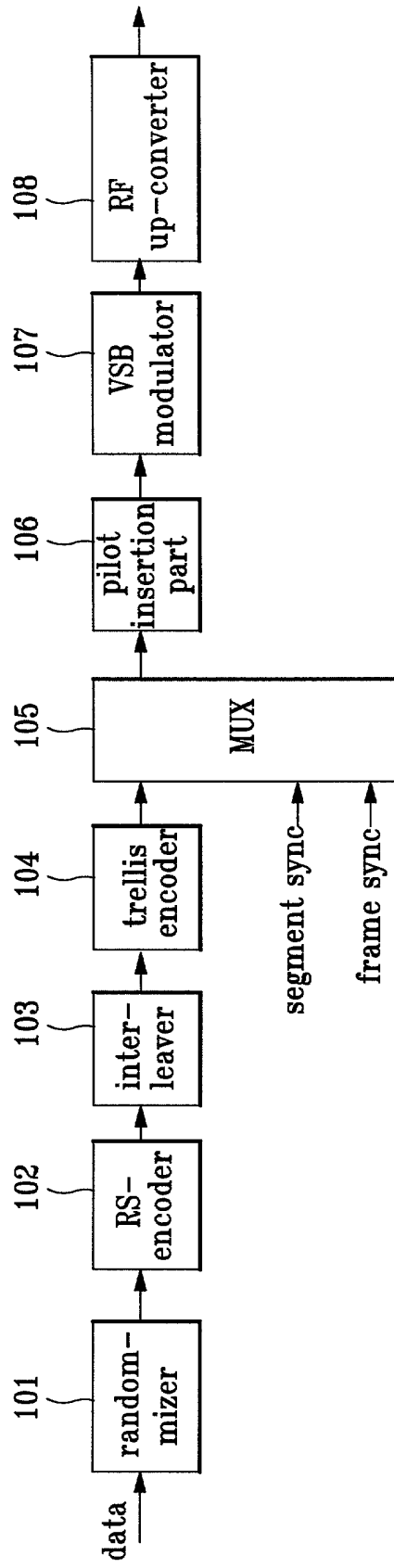


FIG.2a
related art

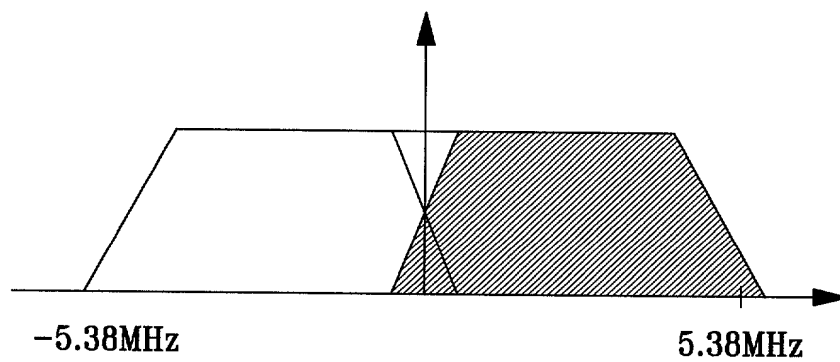


FIG.2b
related art

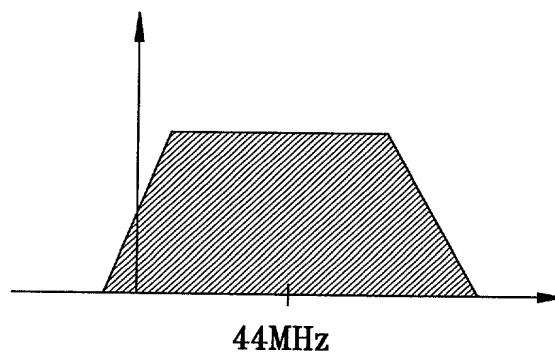
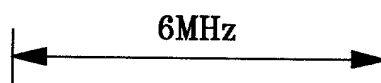


FIG. 3
related art

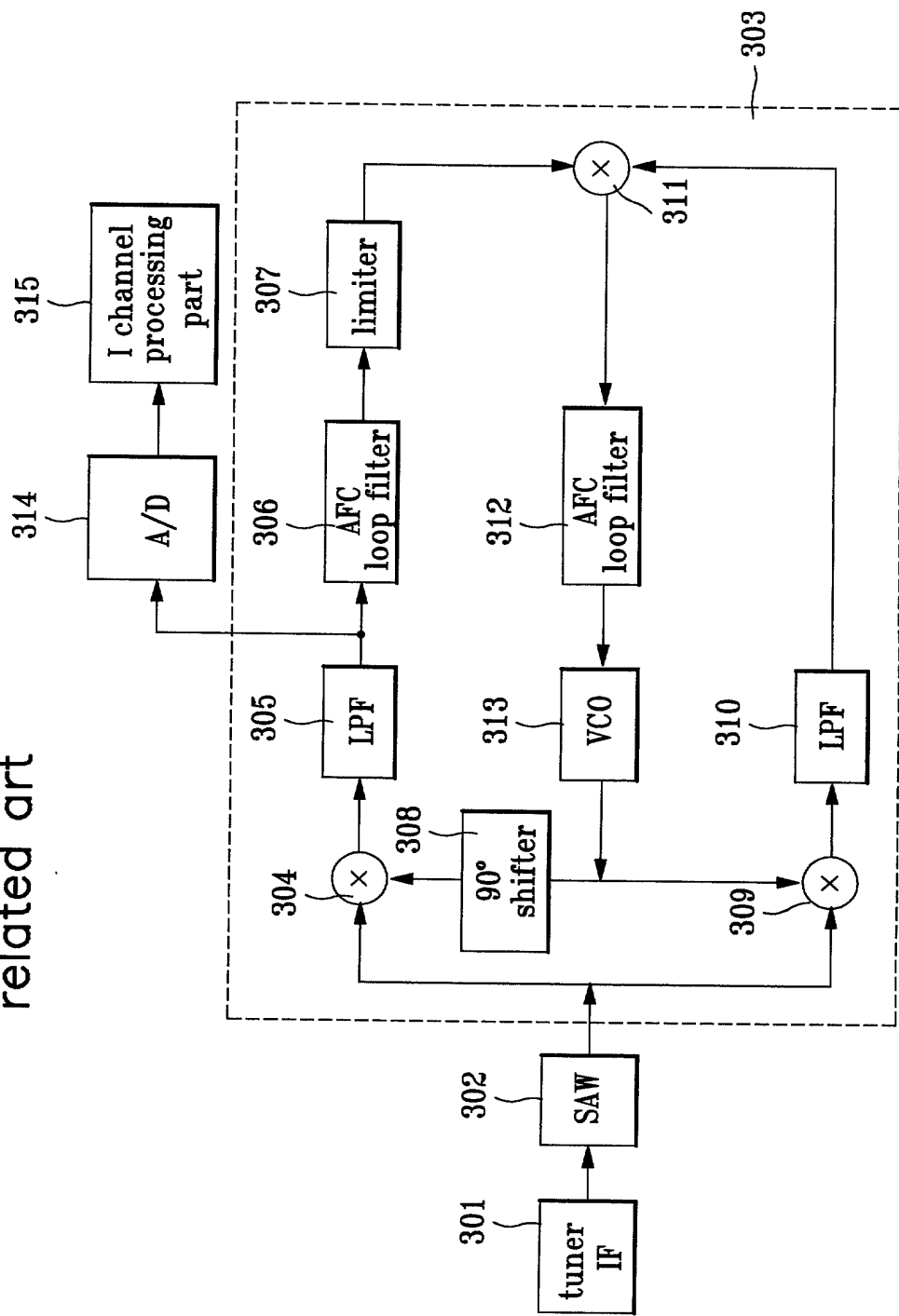


FIG.4a
related art

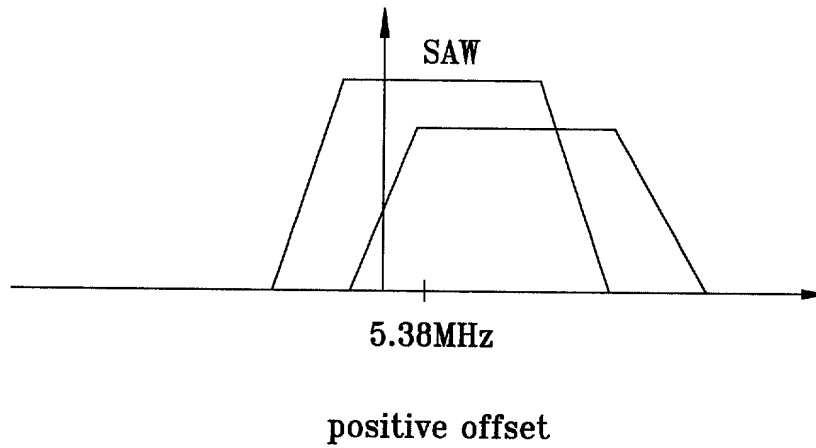


FIG.4b
related art

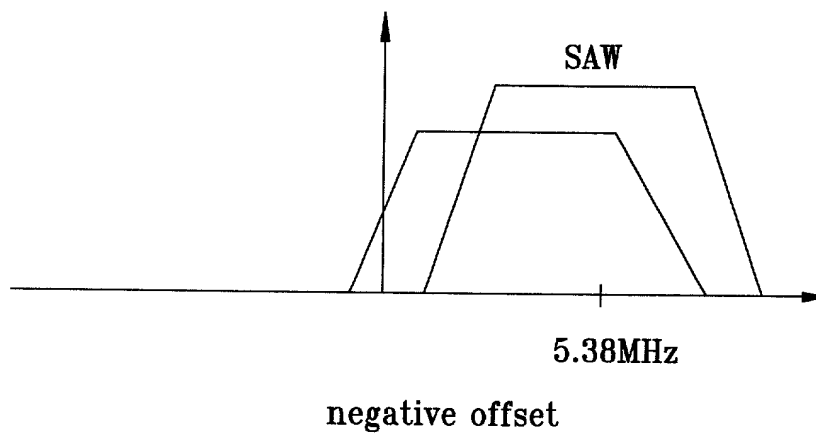


FIG.5
related art

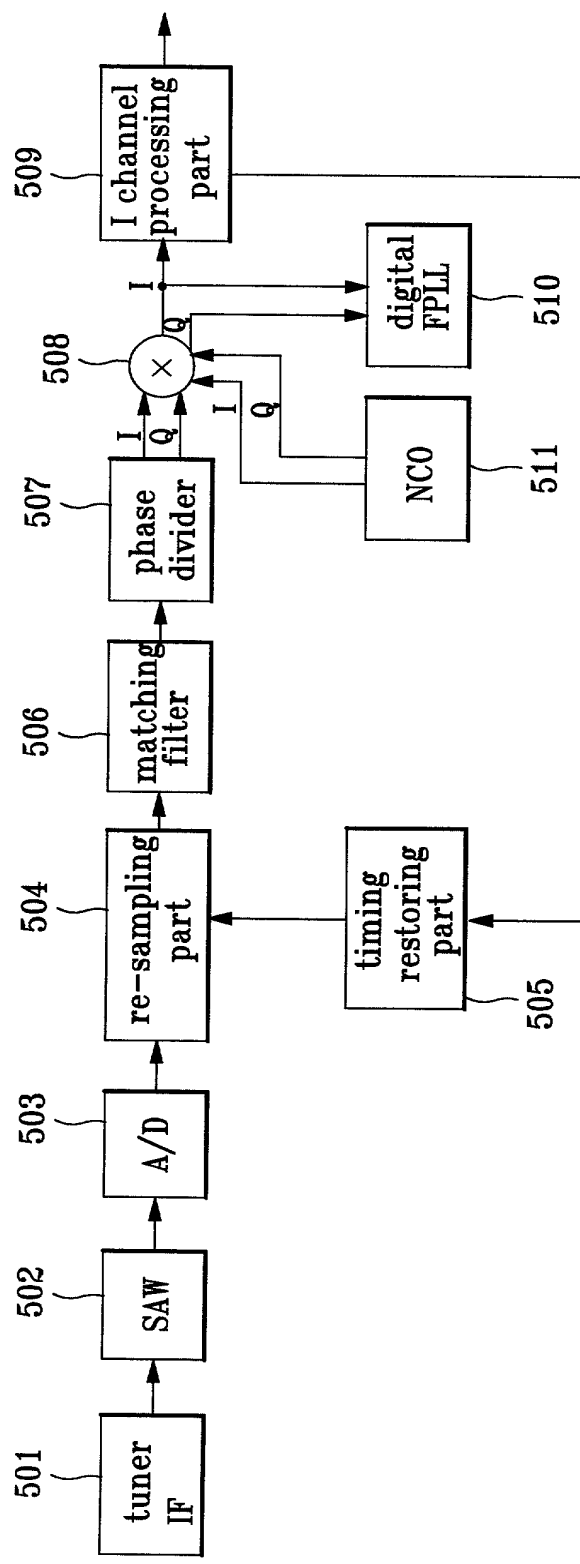


FIG. 6

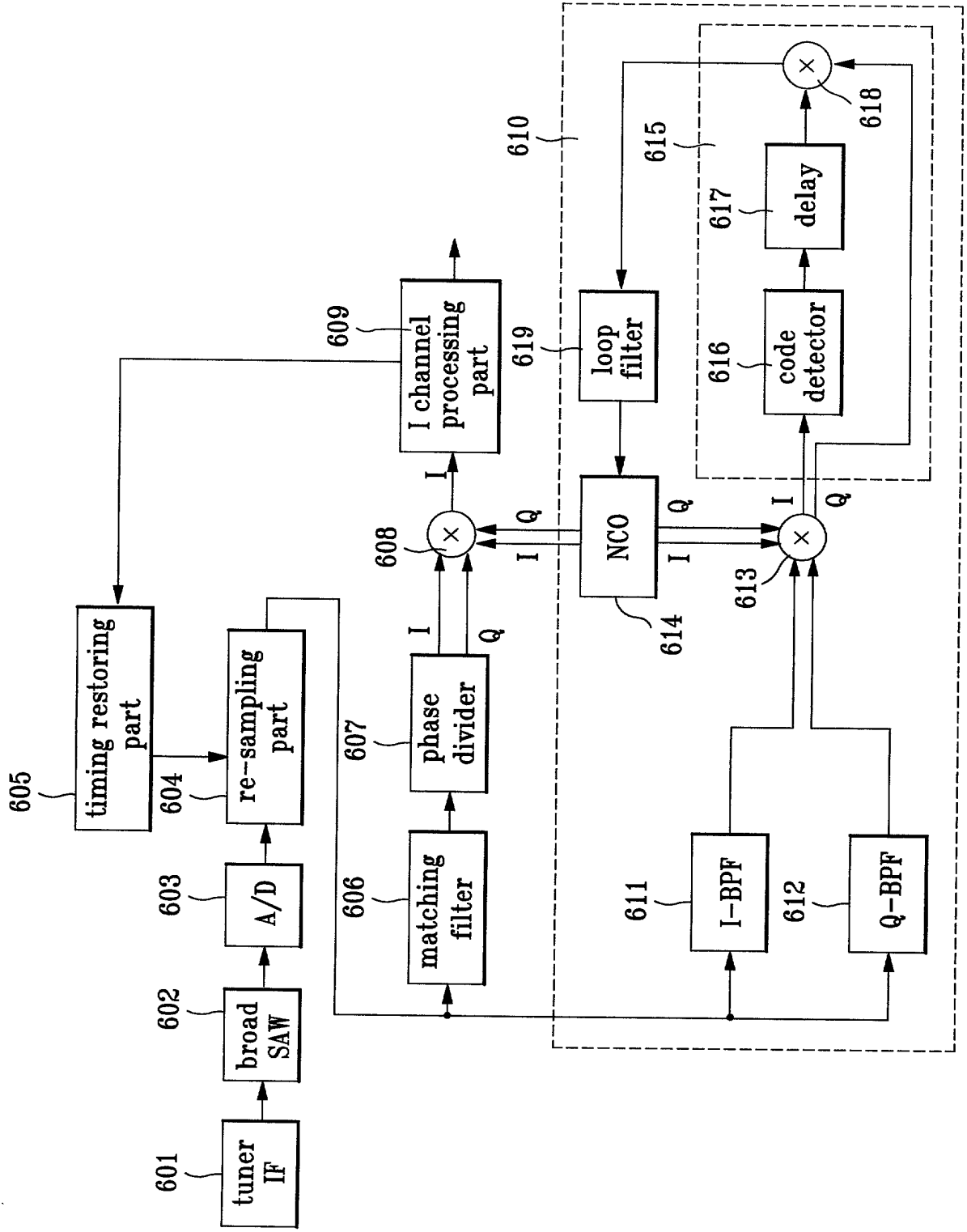
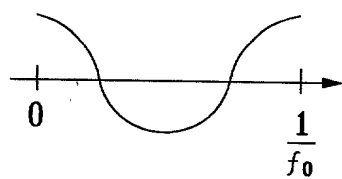
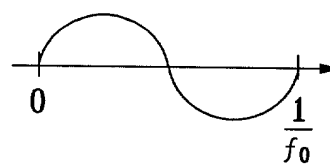


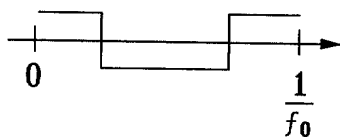
FIG.7



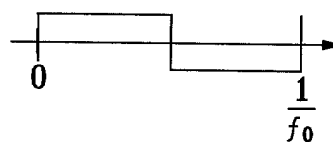
(a)component I



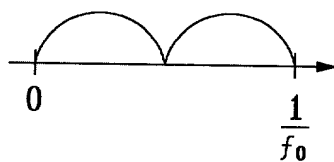
(b)component Q



(c)code value of
component I

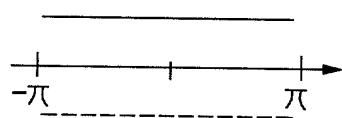


(d)code value of
delayed component I

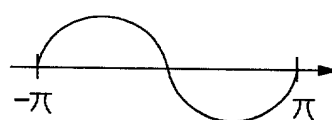


(e)code value of delayed phase
component I * component Q

FIG.8



(a)component I



(b)component Q

FIG.9

